

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Johannes Becker

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Title: SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT  
AS AGAINST SUBSEQUENT REPROGRAMMING

Grp./A.U.: 2135

Examiner: Suman Debnath

Confirmation No.: 6816

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

I hereby certify that this correspondence is being electronically filed  
with United States Patent and trademark Office on:

November 21, 2007 (Date)

Debbie Sams

(Printed or typed name of person signing the certificate)

/Debbie Sams/

(Signature of the person signing the certificate)

**PRELIMINARY AMENDMENT SUBMITTED WITH RCE UNDER 37 C.F.R. §1.114(c)**

Before continuing examination of the above-identified application, please refer to the below  
arguments and amend the claims as indicated.